

FIG. 1

TrCH NUMBER	RM i	THE NUMBER OF BITS PER FRAME BEFORE RATE MATCHING
TrCH#1	256	270
TrCH#2	250	690
TrCH#3	240	540
TrCH#4	200	600

FIG. 2

TrCH NUMBER	ΔN_{ij}
TrCH#1	+67(Repetition)
TrCH#2	+153(Repetition)
TrCH#3	+93(Repetition)
TrCH#4	- 13(Puncture)

FIG. 3

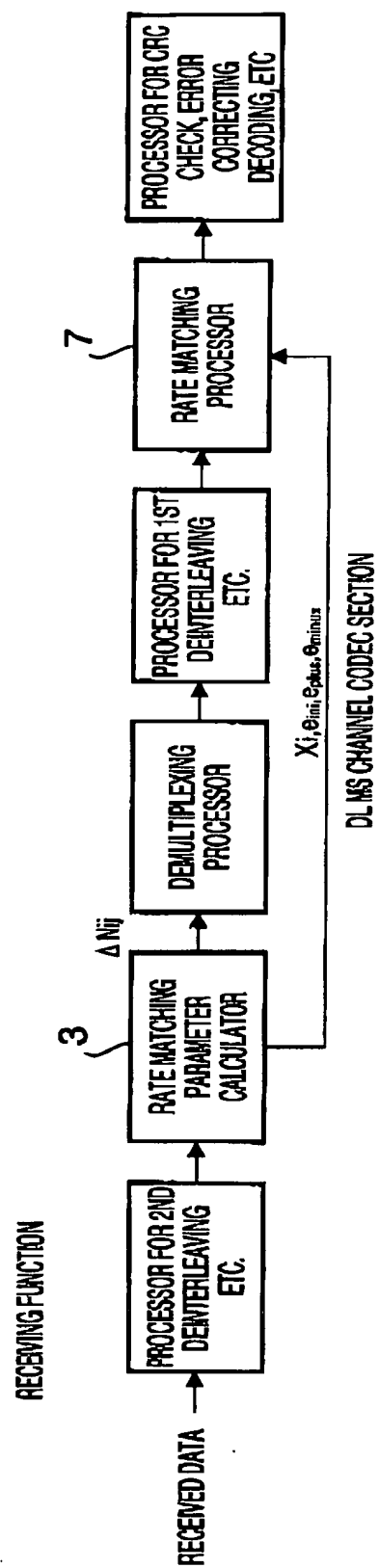
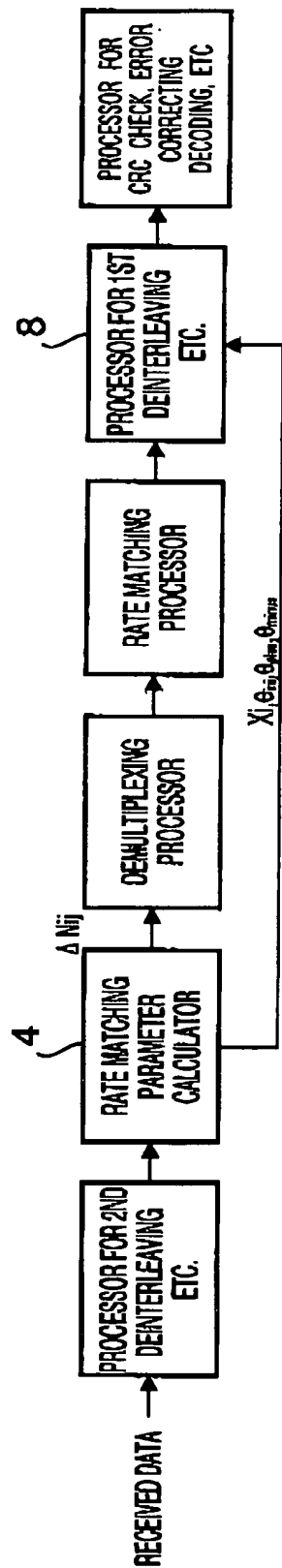


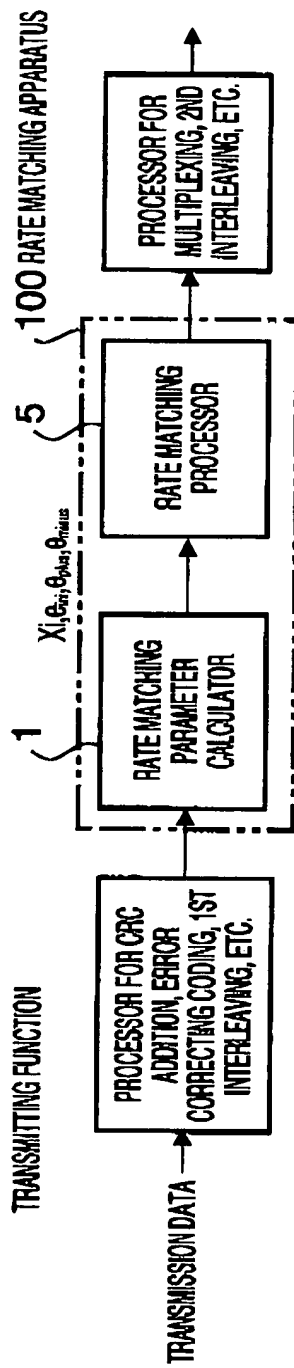
Fig. 4



UL BTS CHANNEL CODEC SECTION

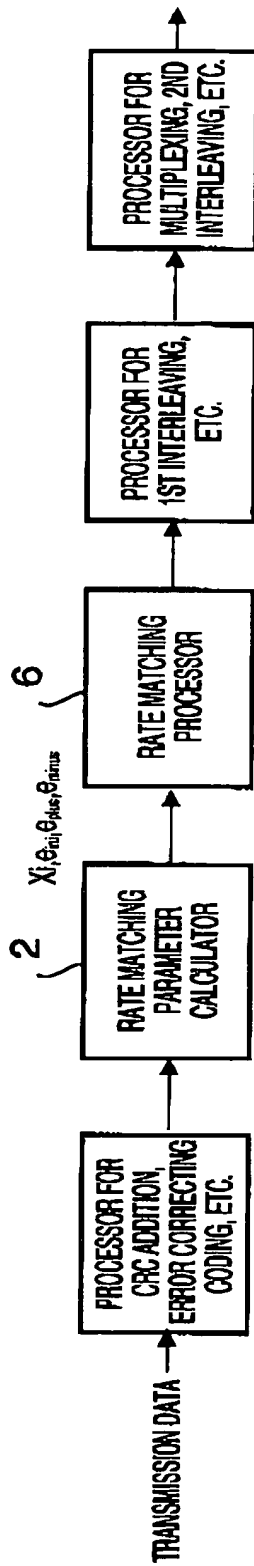
FIG. 5

1. The system of claim 1, wherein the rate matching processor is configured to perform rate matching based on a rate matching parameter calculated by the rate matching parameter calculator.



UL MS CHANNEL CODEC SECTION

FIG. 6



DL BTS CHANNEL CODEC SECTION

FIG. 7

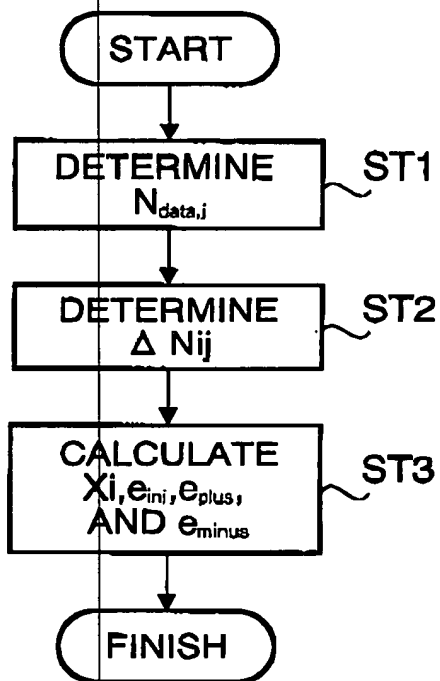


FIG. 8

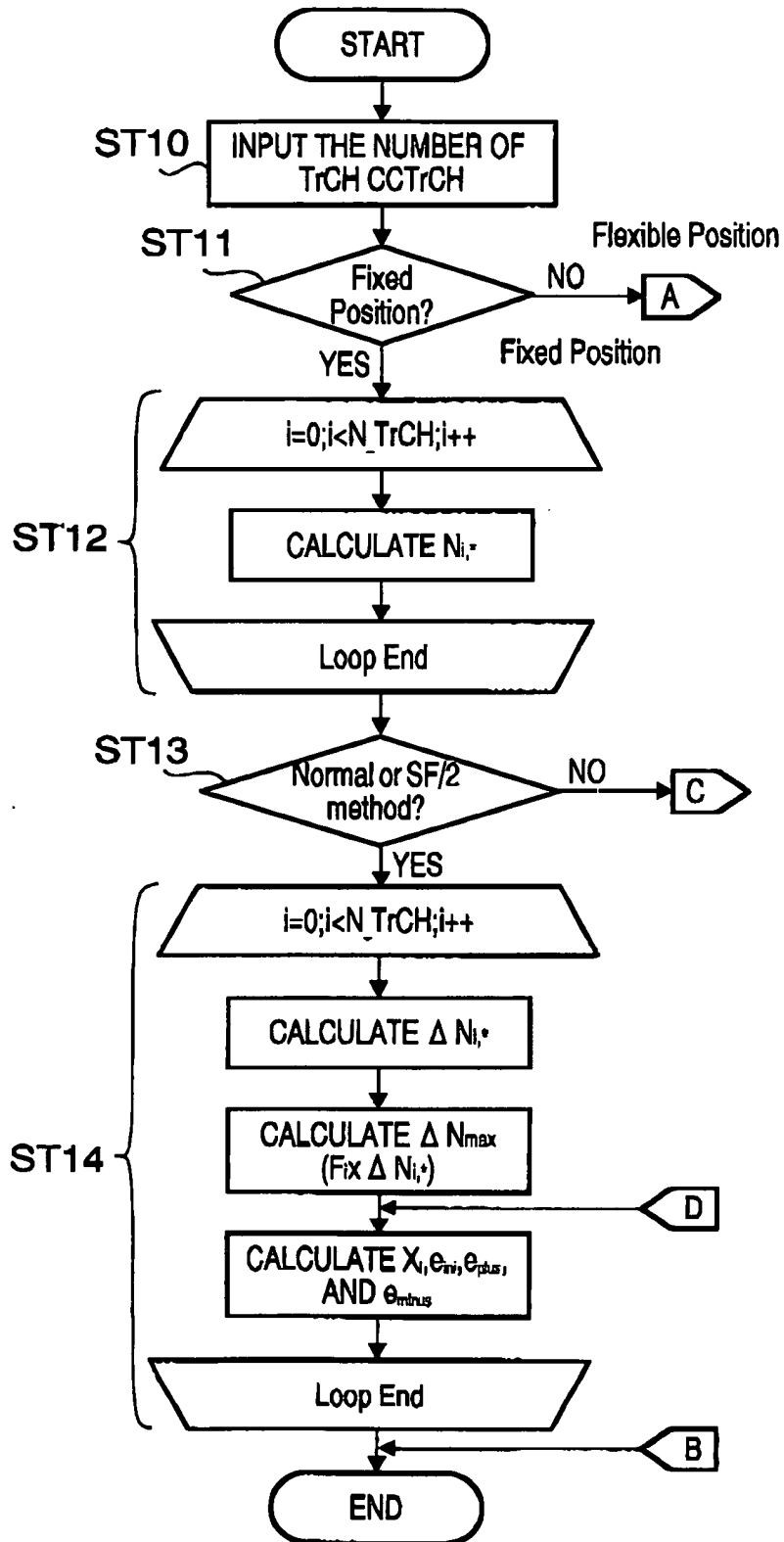


FIG. 9

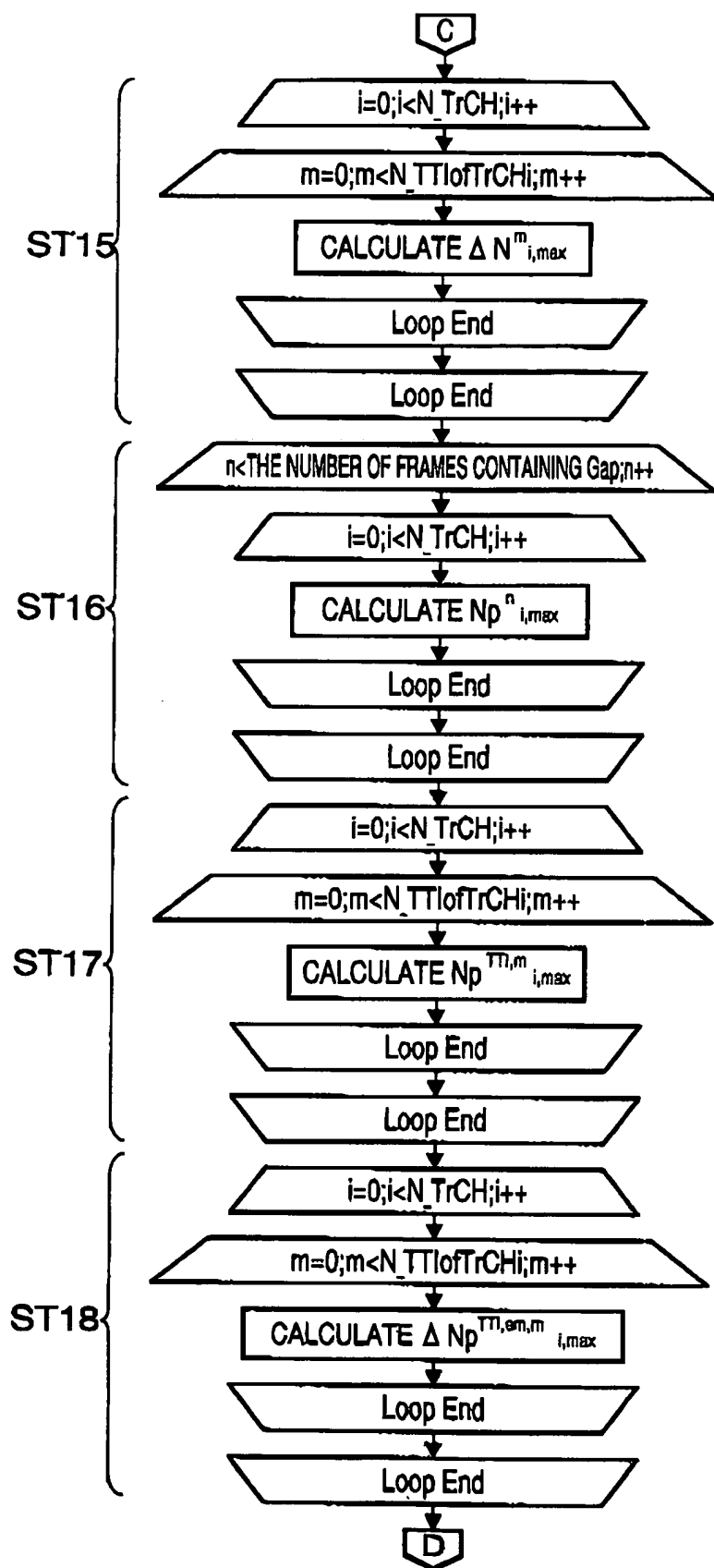


FIG. 10

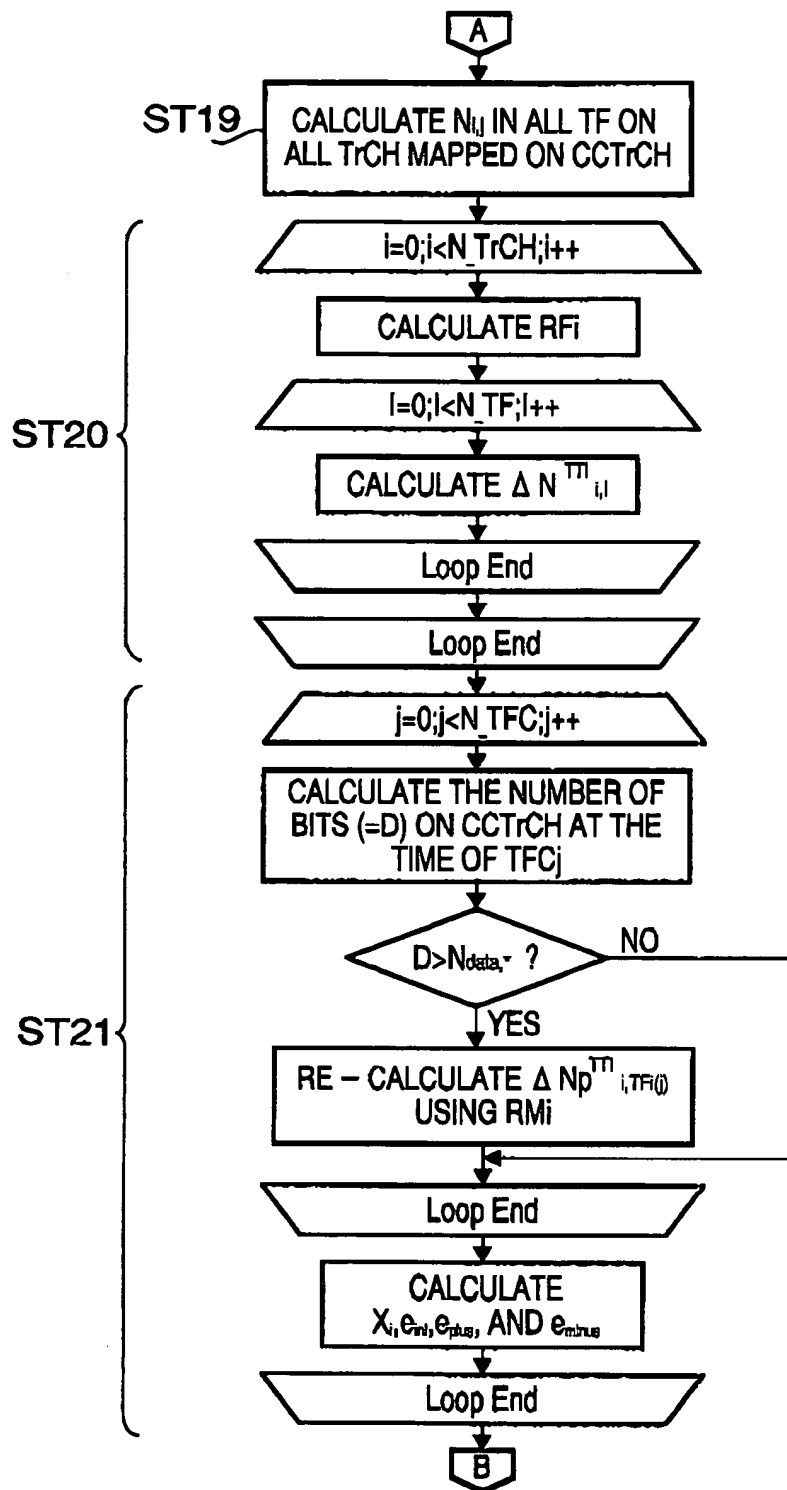


FIG.11

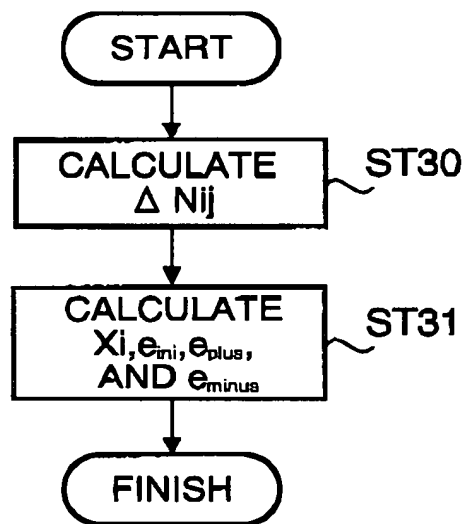


FIG.12

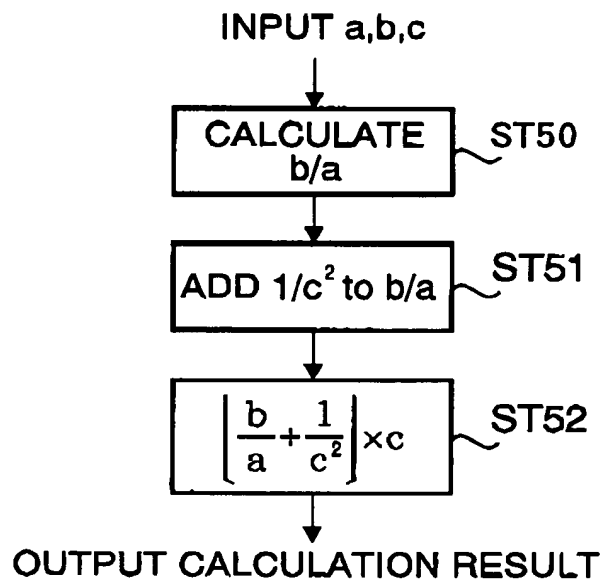


FIG.13

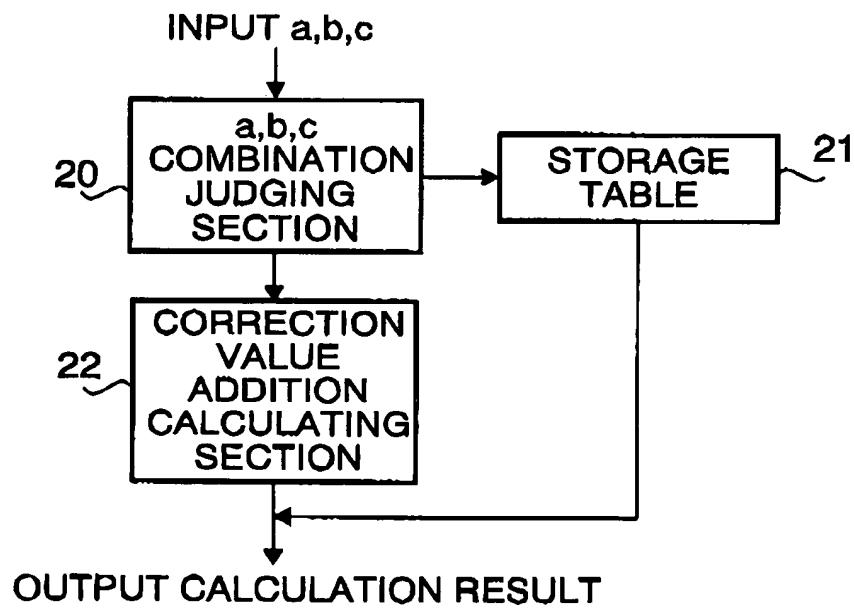


FIG.14

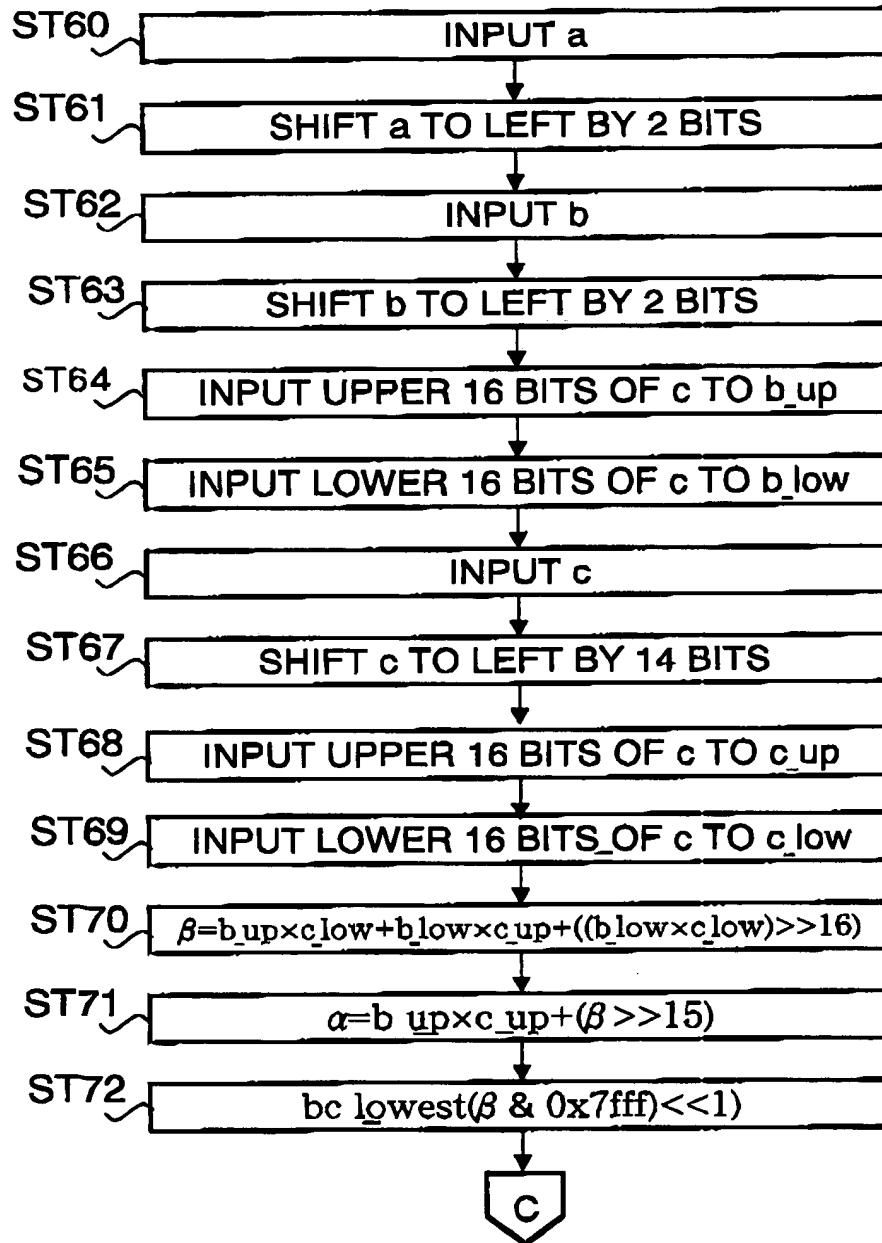


FIG.15

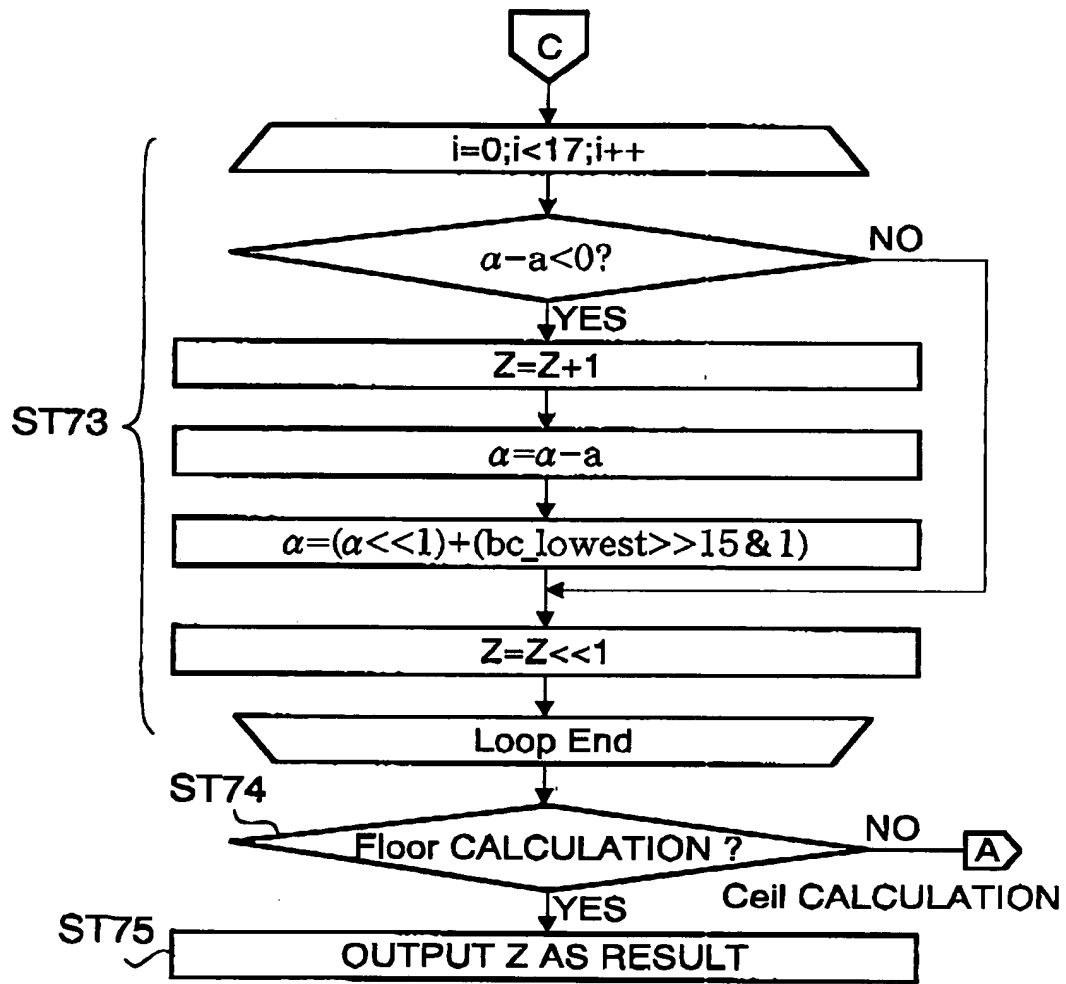


FIG. 16

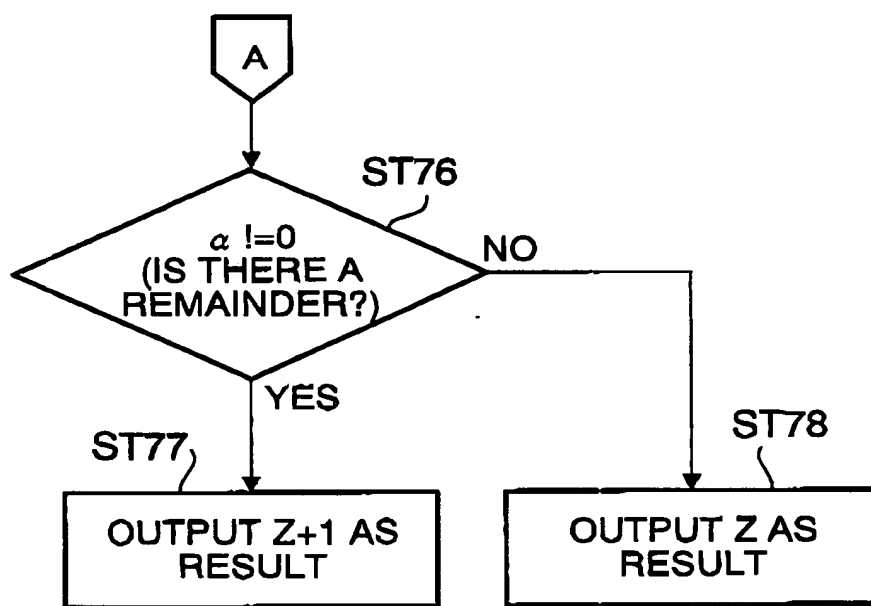


FIG.17

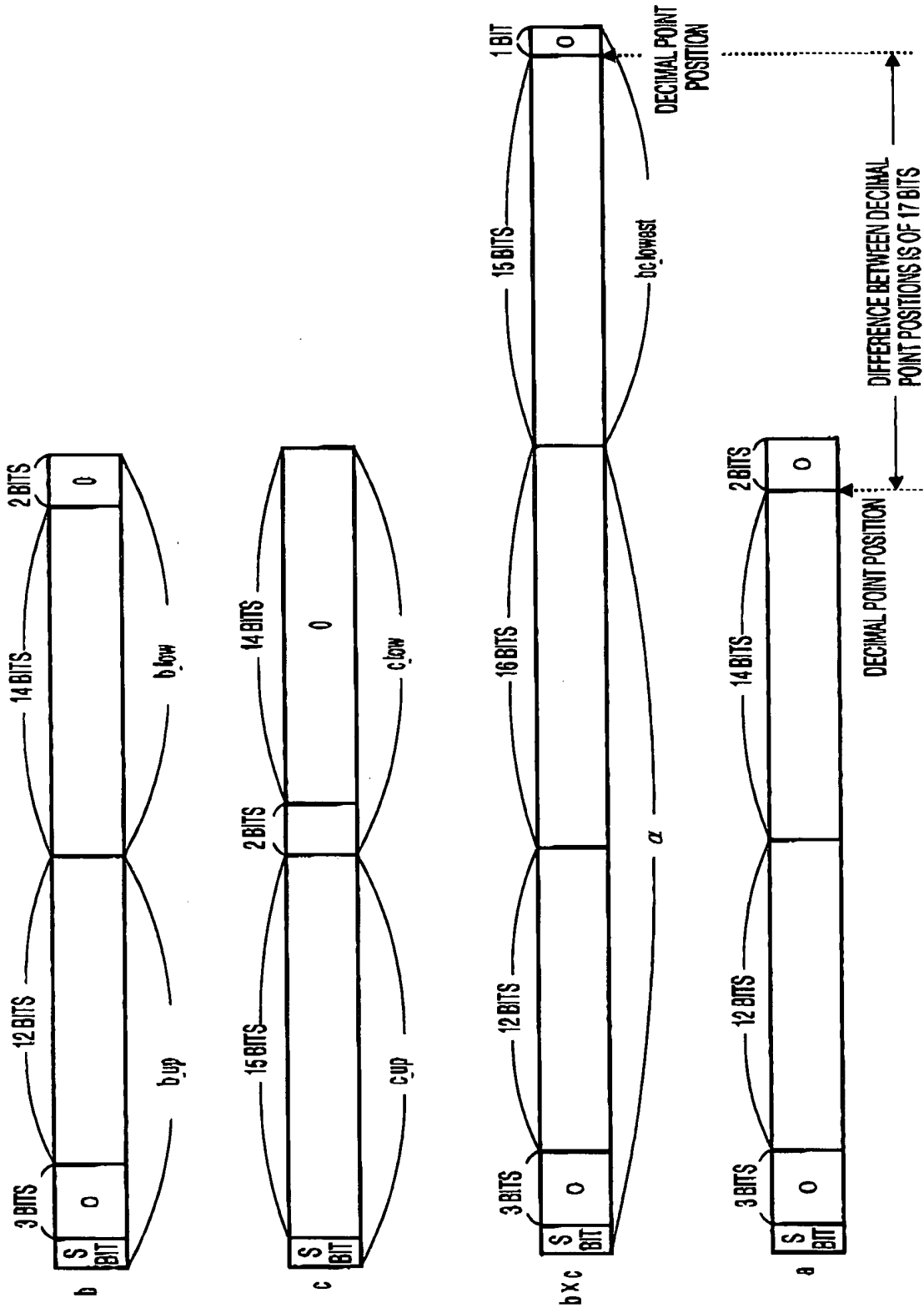


FIG.18